## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 6,934,829 B2 DATED : August 23, 2005

Page 1 of 1

INVENTOR(S) : August 23, 2005

: August 23, 2005

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

## Title page,

Item [56], References Cited, OTHER PUBLICATIONS, "B-Bit Embedded controller Handbook..." should read -- 8-Bit Embedded controller Handbook --.

## Column 56,

Line 14, "...the instruction arid a register..." should read -- the instruction and a register --.

Signed and Sealed this

Thirty-first Day of January, 2006

JON W. DUDAS Director of the United States Patent and Trademark Office